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		STUDY MODULE D	ESC	PIDTION FORM			
Name of the module/subject Programmable logic controllers				Code 1010311271010321903			
Field of study  Electrical engineering			(	Profile of study (general academic, practical)  Year /Semester			
Elective path/specialty				<u> </u>		Course (compulsory, elective)	
		-		polish		obligatory	
Cycle o	f study:		Form	of study (full-time,part-time)			
First-cycle studies				full-time			
No. of h	nours					No. of credits	
Lectu	re: 1 Classe:	s: - Laboratory: 1	Pr	oject/seminars:	1	5	
Status		program (Basic, major, other)	(un	(university-wide, from another field)			
		(brak)			(bra	ak)	
Educati	on areas and fields of sci	ence and art				ECTS distribution (number and %)	
techi	nical sciences					5 100%	
	Technical sciences					5 100%	
dr inż. Michał Krystkowiak email: Michal.Krystkowiak@put.poznan.pl tel. +48616652388 Electrical Piotrowo 3a, 60-965 Poznan							
Prere	equisites in term	is of knowledge, skills an	nd soc	ial competencies:			
1	Knowledge			ters of programmable logic controllers PLC. He knows the sted PLC programming languages??. Basic knowledge of			
2	Skills	Know how to program and opera	ate at a general level programmable logic controllers.				
3	Social competencies	He can think and act in an entrepreneurial manner in the design of industrial automation systems					
Assu	mptions and obj	ectives of the course:					
	g familiar with the oper ation systems.	ration, maintenance and programr	ming of	PLCs. Acquisition of the	abili	ity to design industrial	
	Study outco	mes and reference to the	educ	ational results for	a fi	ield of study	
Knov	vledge:					<u> </u>	
		e the principles of operation of realicate their industrial applications -		systems, including syste	ems b	pased on programmable	
2. Sho	uld be able to: choose	programming languages??, tools	s, runtir	ne and communication p	rotoc	cols PLC - [-]	
Skills	s:						
1. Will be able to: apply knowledge of such industrial automation to develop and implement specific algorithms PLC - [-]							
2. Wil [-]	be able to: apply the	selected simulation tools and deve	elopme	ent environments to supp	ort d	lesign automation systems	
Social competencies:							

# Assessment methods of study outcomes

1. He can think and act in an entrepreneurial manner in the design of electronic systems-processor - [-]

# Faculty of Electrical Engineering

#### Lecture:

? assess the knowledge and skills listed on the written exam with a test and problematic, continuous evaluation for each course (rewarding activity and quality perception)

Design classes and laboratory exercises:

- ? test and favoring knowledge necessary for the accomplishment of problems in the area of tasks in the laboratory,
- ? continuous evaluation, rewarding gain skills they met the principles and methods
- ? assess the knowledge and skills related to the implementation of laboratory exercises, the evaluation report made ??exercise.

Get extra points for the activity in the classroom, and in particular for:

- ? propose to discuss further aspects of the subject,
- ? the effectiveness of the application of the knowledge gained during solving the given problem,
- ? ability to work within a team performing a task specific practice in the laboratory.

### **Course description**

The concepts of real-time system and programmable PLC. Application possibilities PLC systems. Architecture of programmable industrial controllers and their classification. Characteristics of the program cycle, programmable logic controllers. PLC runtime tools - programming languages ??(LAD, STL, FBD). Characteristics of basic PLC expansion modules. Complex systems, programmable logic controllers - communication protocols. Visualization and process control automation from a PC.

### Basic bibliography:

- 1. J. Kwaśniewski? Sterowniki PLC w pracy inżynierskiej, PTC, Kraków 2008.
- 2. T. Legierski , J. Kasprzyk, J. Wyrwał, J. Hajda ? Programowanie sterowników PLC. Wyd. Prac. Komp. J. Skalmierskiego, Gliwice, 1998.
- 3. A. Król , J. Moczko-Król ? S5/S7 Windows. Programowanie i symulacja sterowników PLC firmy Siemens. Wyd. Nakom, Poznań, 2000.

### Additional bibliography:

- 1. S. Brock? Sterowniki programowalne, Wyd. Politechniki Poznańskiej, 2000
- 2. Dokumentacja techniczna sterowników PLC firmy Siemens

# Result of average student's workload

Activity	Time (working hours)
1. Lectures, labs, projects, consultation, examination	48
2. Laboratory classes, design classes, preparation for classes, reports, project	35

### Student's workload

Source of workload	hours	ECTS
Total workload	70	5
Contact hours	48	3
Practical activities	35	3